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# PMB-2.2.1 Benchmarking on POWER4+ Platforms p655+ and p690+

## Problem

The benchmark performance on the POWER4+™ platforms p690+ and p655+ platforms was evaluated using the PMB-2.2.1 benchmark written by Pallas.

## Proposed solution

Pallas has written a comprehensive set of MPI benchmarks, known as PMB, which has the following objectives:

- ▶ Providing a concise set of benchmarks for measuring the MPI functions of point-to-point message-passing, global data movement and computation routines, one-sided communications, file I/O
- ▶ Establishing precise benchmark procedures, including run rules, a set of required results, repetition factors and message lengths
- ▶ Avoiding interpretation of the measured results: execution time, throughput, global operations performance

For a complete explanation and interpretation of PMB benchmark results, refer to the PMB-MPI1.pdf and to the PMB-MPI2.pdf. For detailed results and output logs, refer to the output and log files in the directory PMB2.2.1-mpi, available at:

<http://www.pallas.com/e/products/pmb/index.htm>

## System configuration

The PMB2.2.1 benchmark was tested on the IBM® POWER4+ platforms p690+ and p655+.

Table 1 lists the details of the configurations of these platforms as used in this benchmark.

Table 1 System and hardware configurations

Configurations		P690+	P655+
Processor		1.7 GHz Power4+	1.5GHz POWER4+
Processors/node		32	
Memory/node		128 GB (8-card)	16 GB (2-card)
Mem(GB)/processor			
Caches	L1	64/32 KB (1-way/2-way)	64 / 32 KB (1-way/2-way)
	L2	1.5 MB/card (4-way)	1.5 MB/card (4-way)
	L3	128 MB	128 MB
OS		AIX® 5.1.0.0	AIX 5.1.0.0
AIX Kernel		64-bit	64-bit
File system(s)		Local or gpfs	Local or gpfs
FORTRAN compiler		XLF 8.1	XLF 8.1
C/C++ compiler		VAC 6.0	VAC 6.0

## Measurement and results

Our testing gave the following results.

### Example 1 Compilation

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```

MPI_HOME      = /usr/lpp/ppe.poe/
MPI_INCLUDE   = $(MPI_HOME)/include
LIBS          = -bmaxdata:0x70000000 -bmaxstack:0x10000000 -lm
CC            = mpcc_r
CLINKER       = mpcc_r
OPTFLAGS      =
CPPFLAGS      = -DnoCHECK

```

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### Example 2 Run script

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```

export MP_EULIB=us
export MP_EUIDEVICE=csss
export MP_INFOLEVEL=0
export MP_SHARED_MEMORY=yes
export MP_STDINMODE=none
export MP_EAGER_LIMIT=65536 #(try this to see if performance can be )
export MP_BUFFER_MEM=67108864 #(set this when MP_EAGER_LIMIT is set)
export MP_WAIT_MODE=poll #(need to set this when MP_EULIB=ip )
export MP_HOSTFILE=host.list
export MP_PROCS=$1
PMB-MPI1 (or PMB-IO, PMB-EXT)

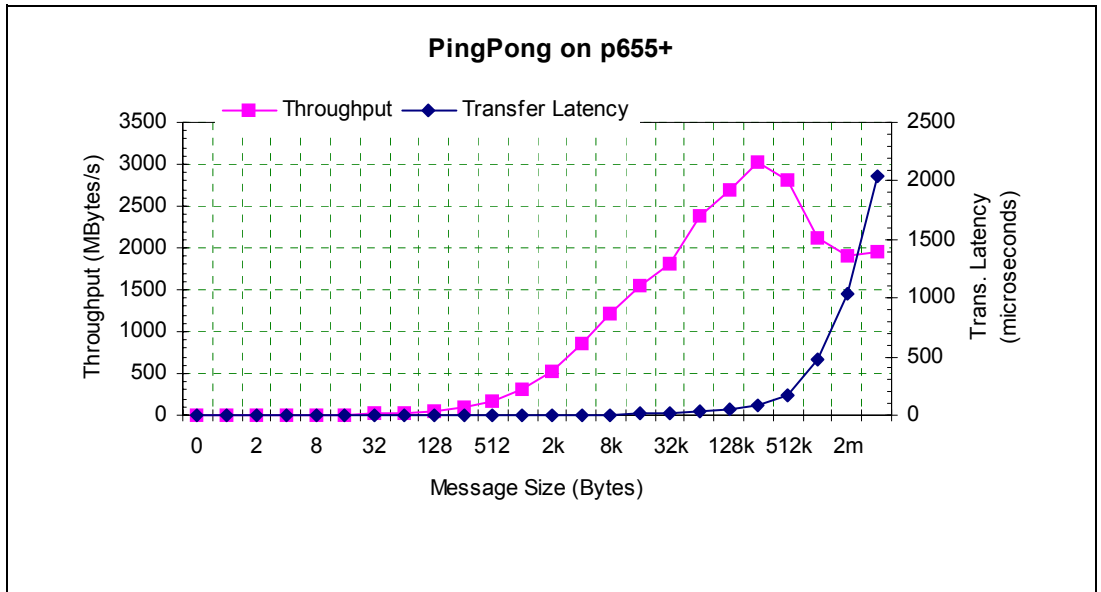
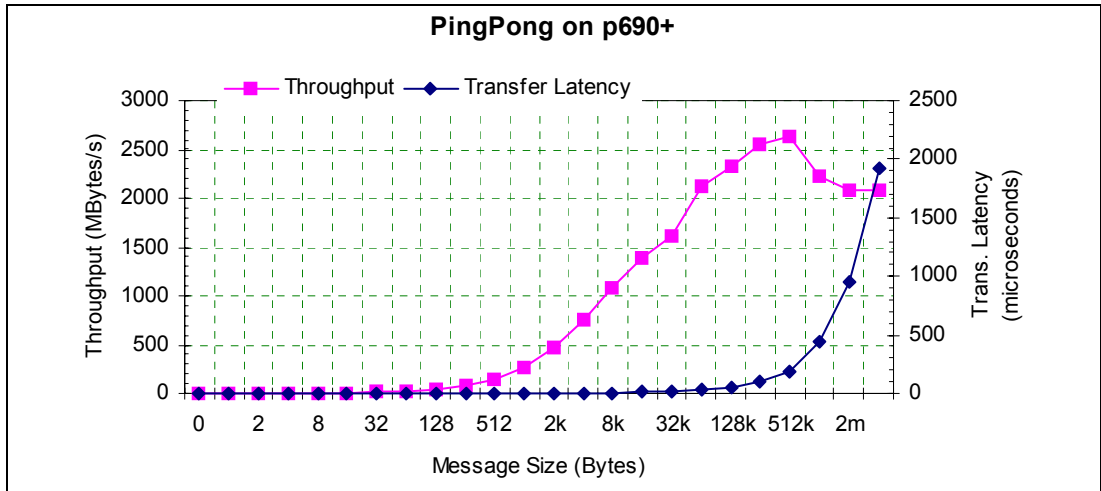
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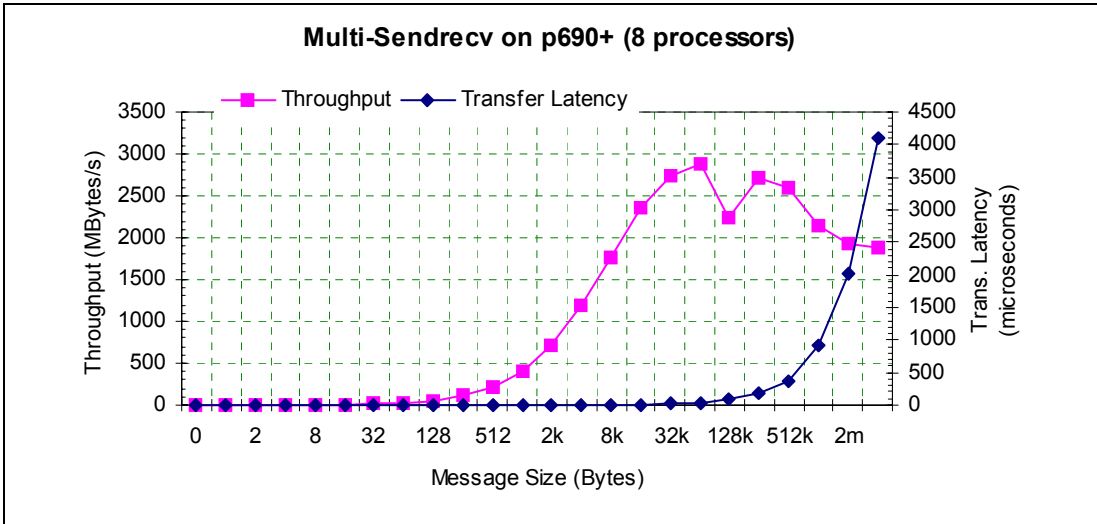
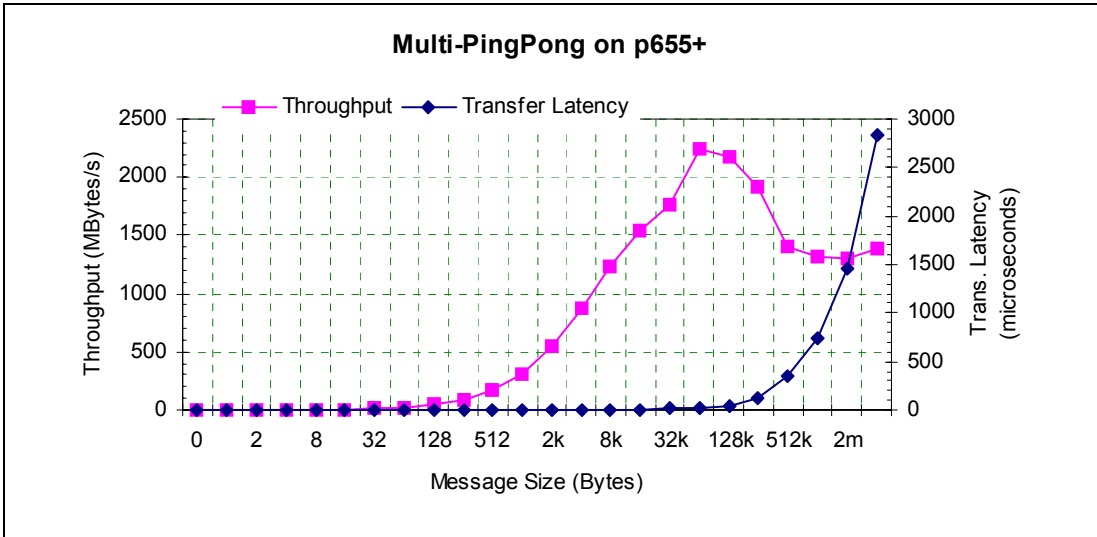
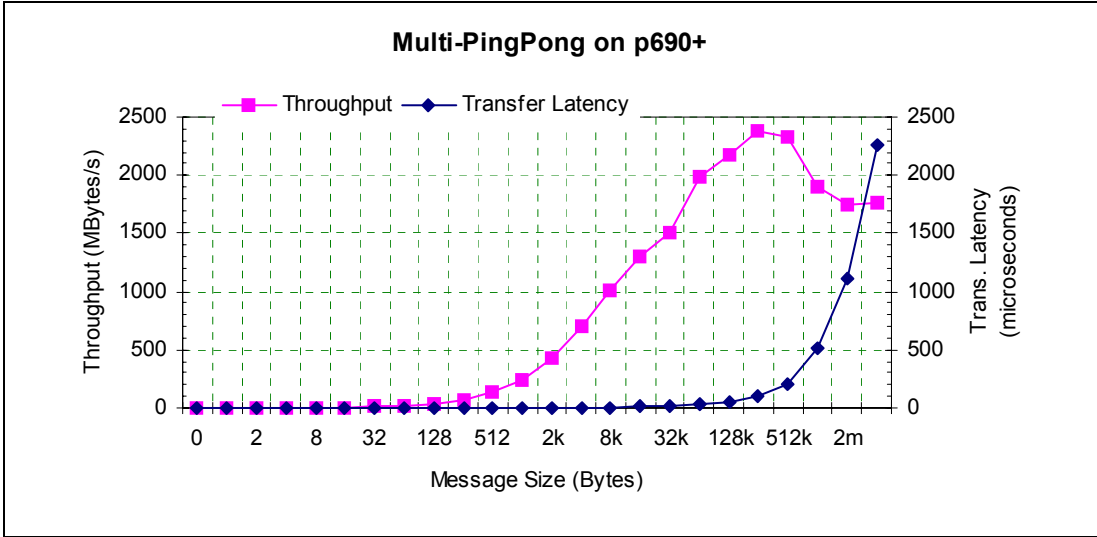
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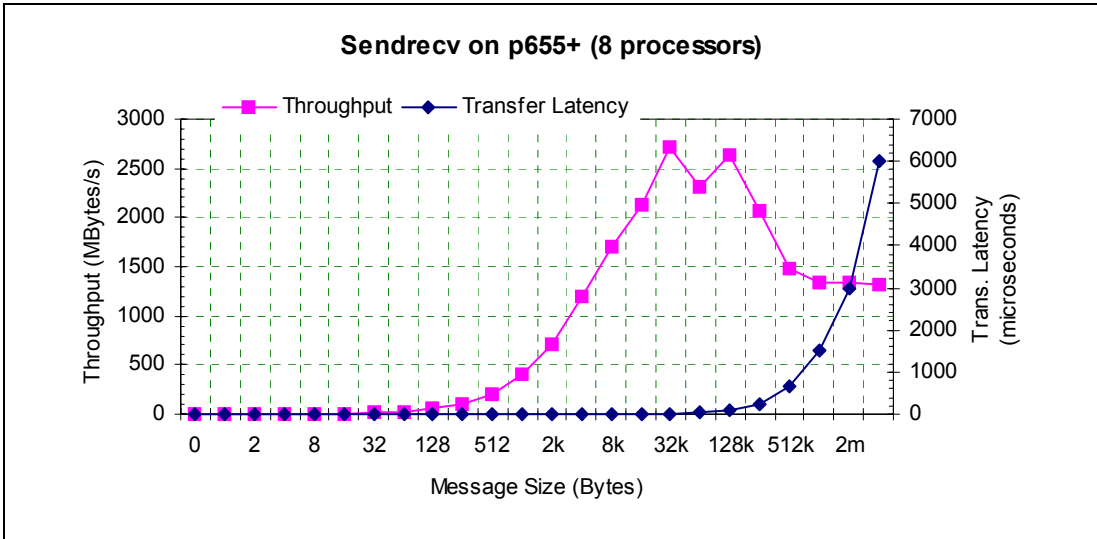
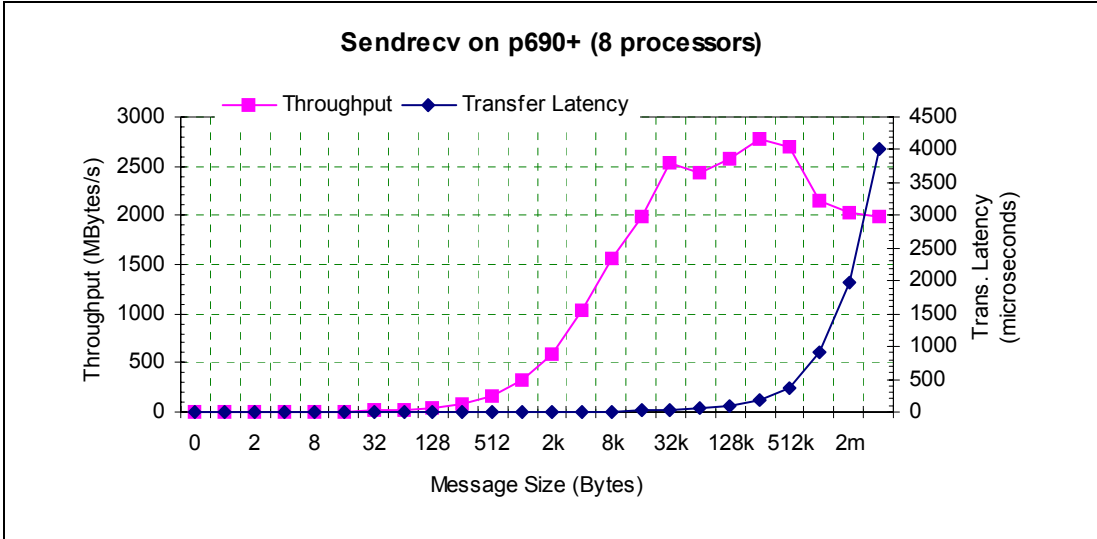
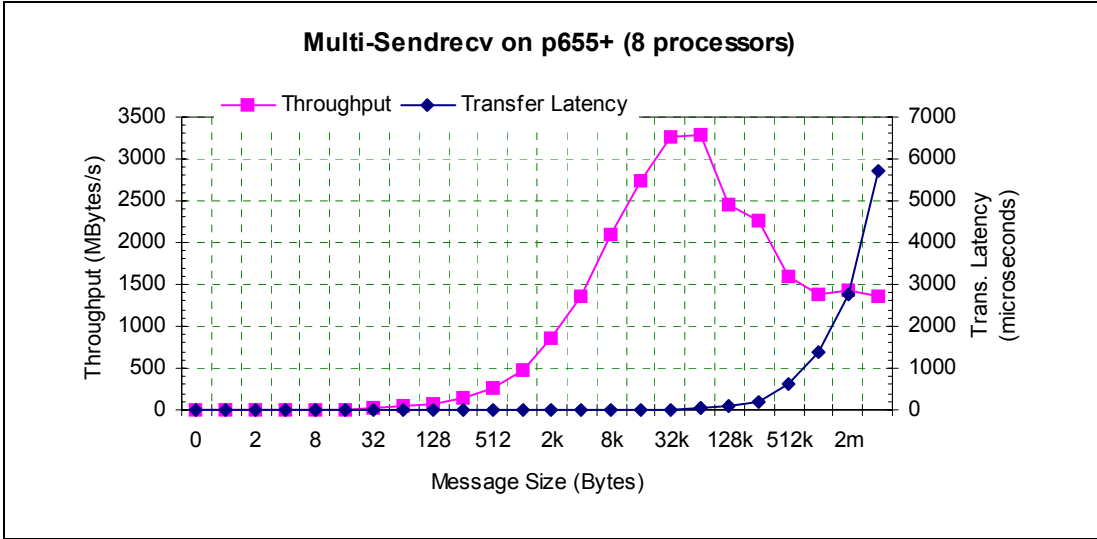
## Point-to-point performance

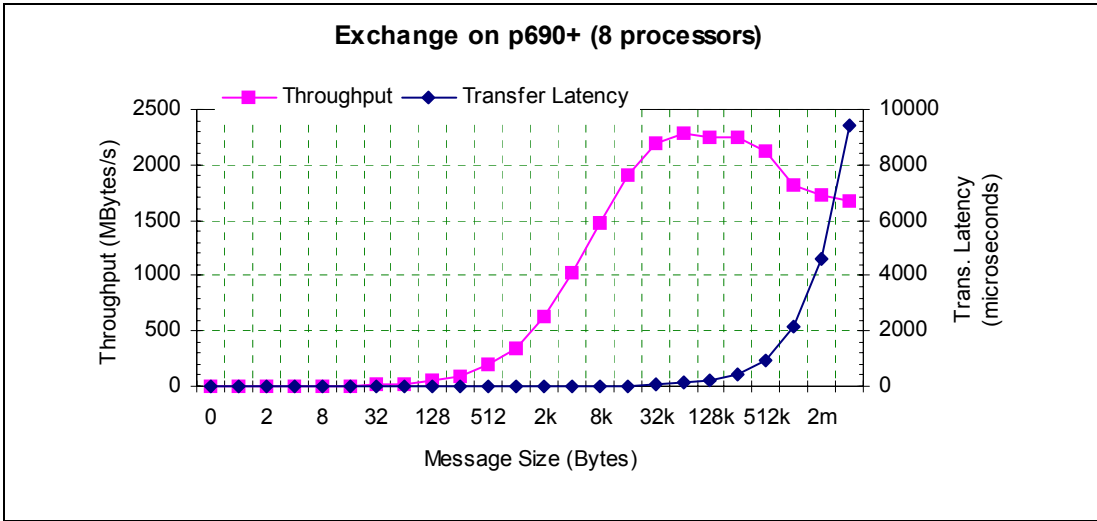
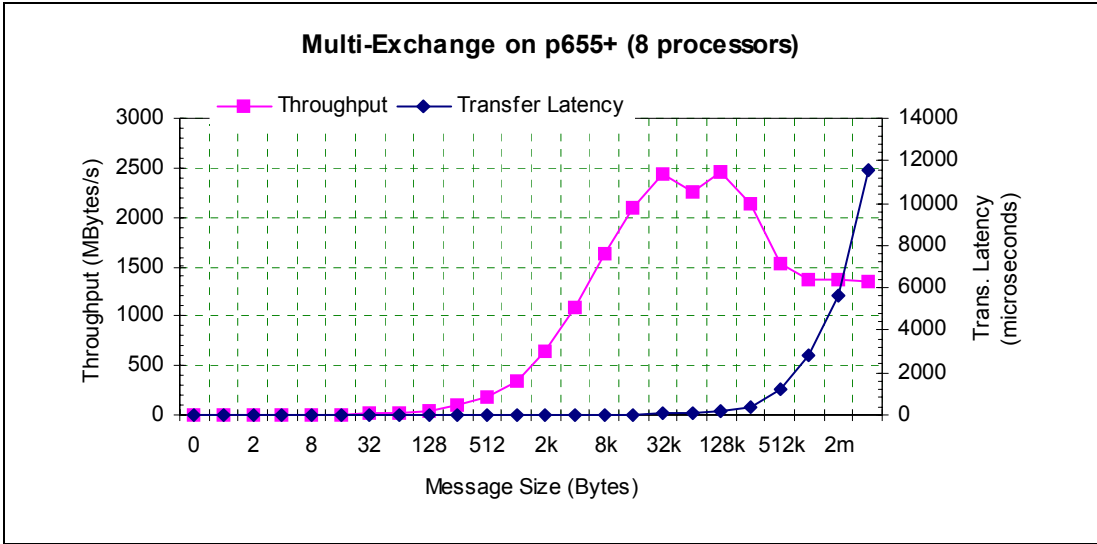
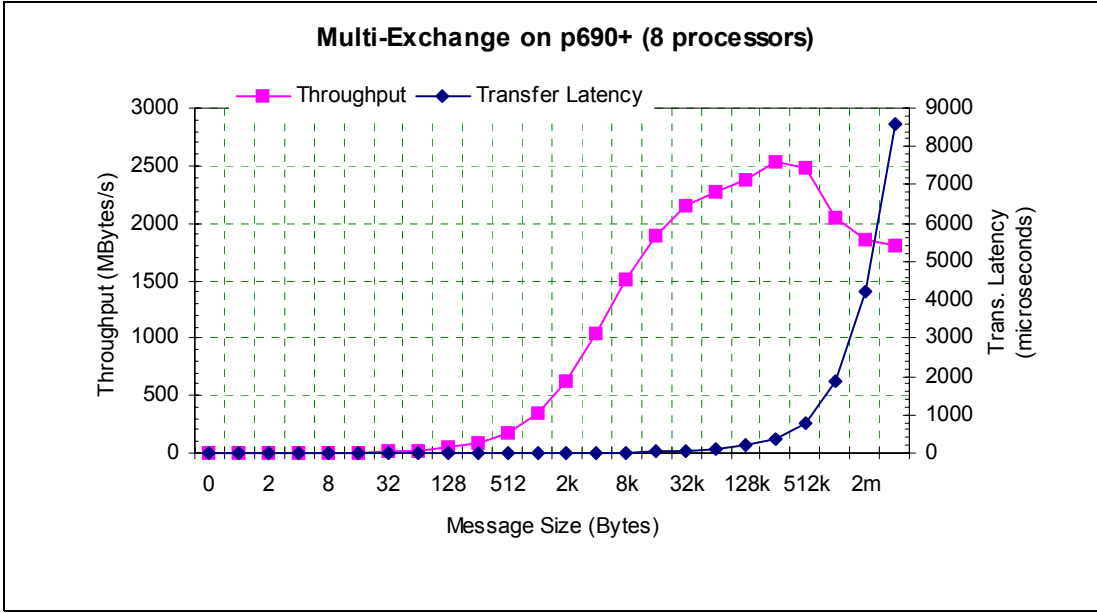
Point-to-point performance is measured between two processes within the same node (memory performance), or between two nodes (network performance). The performance is measured in MBytes/s per process (send+recv) in units of microseconds.

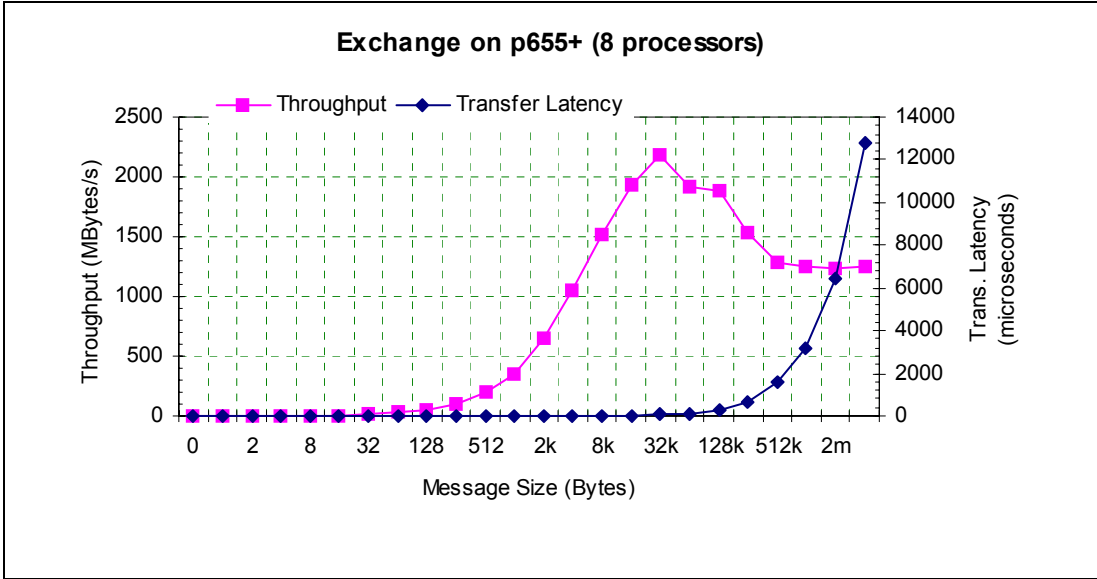
The following series of graphs illustrate the performance of PingPong, Multi-PingPong, Multi-Sendrecv, Sendrecv, Multi-Exchange and Exchange on p690+ and on p655+.







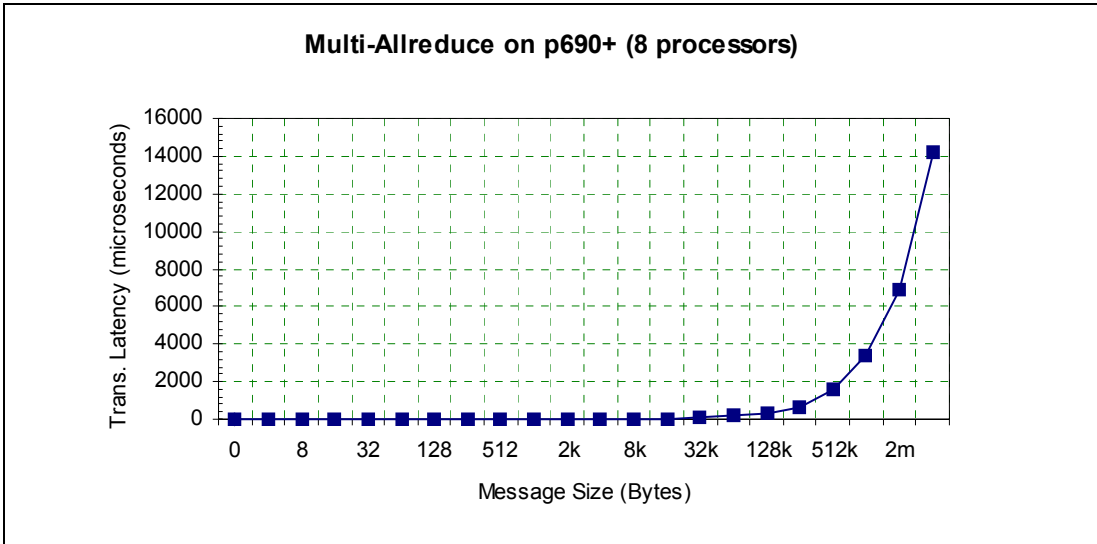


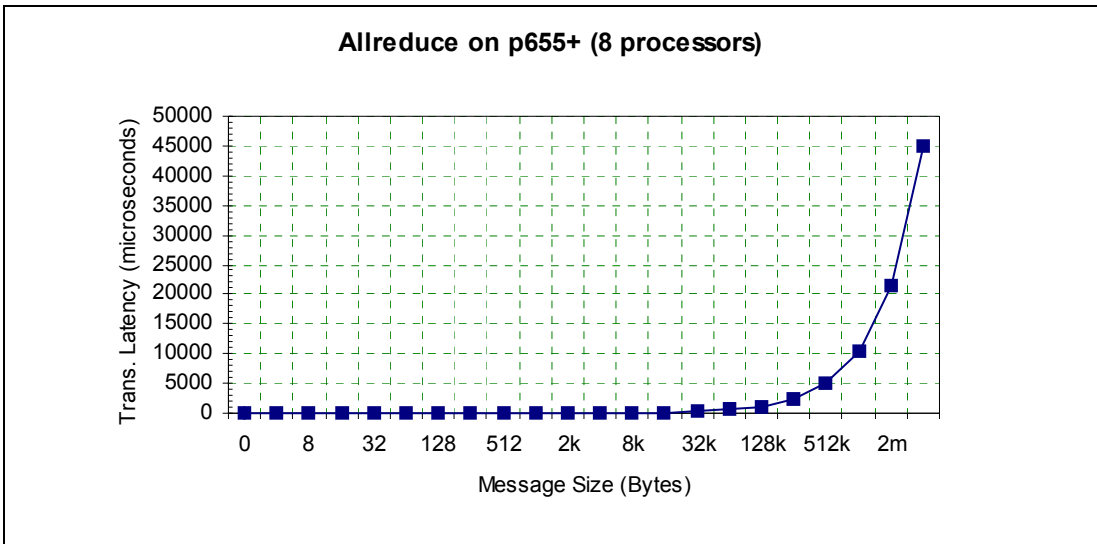
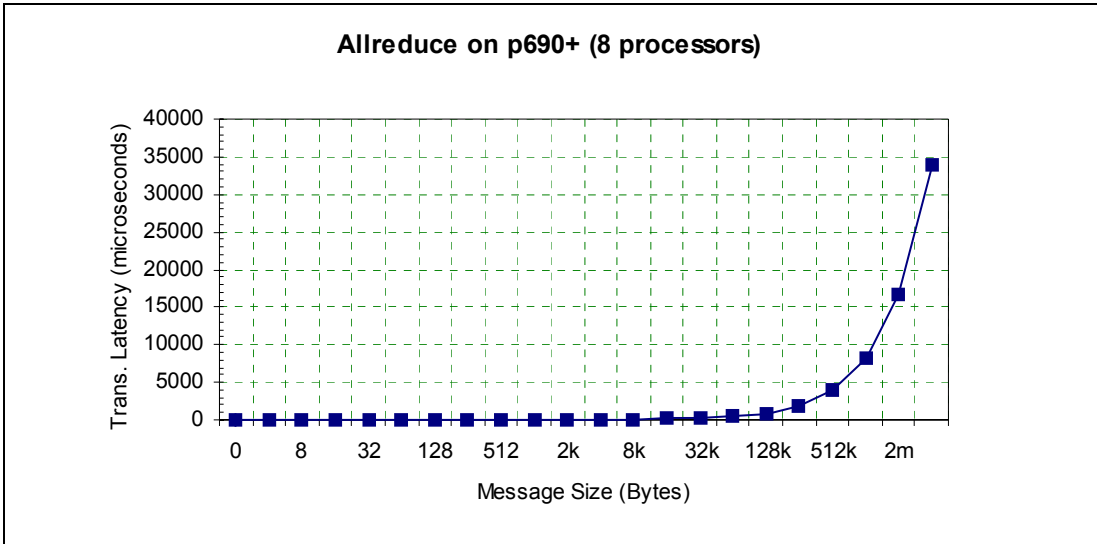
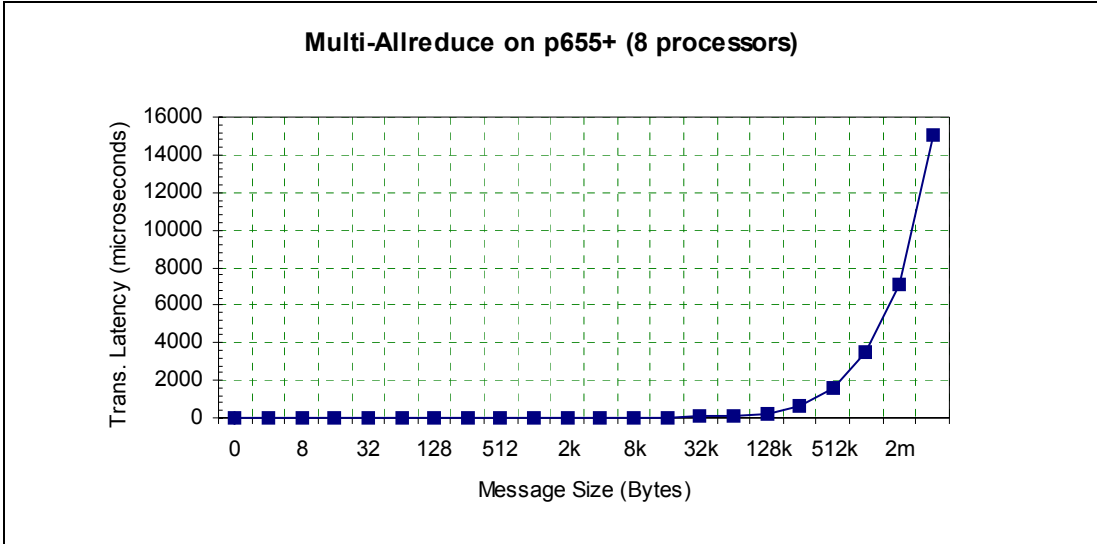


**Collective benchmarks**

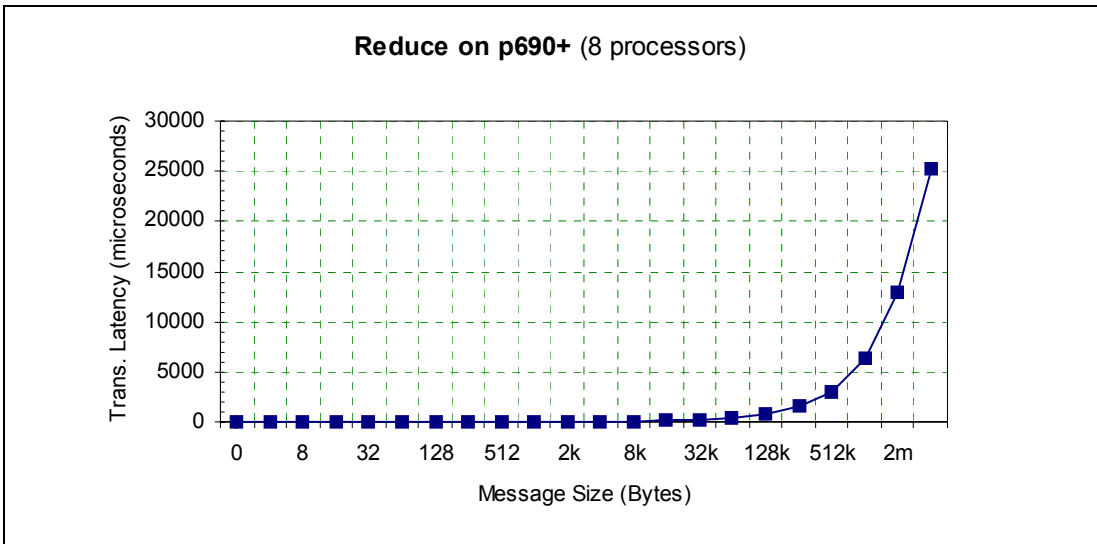
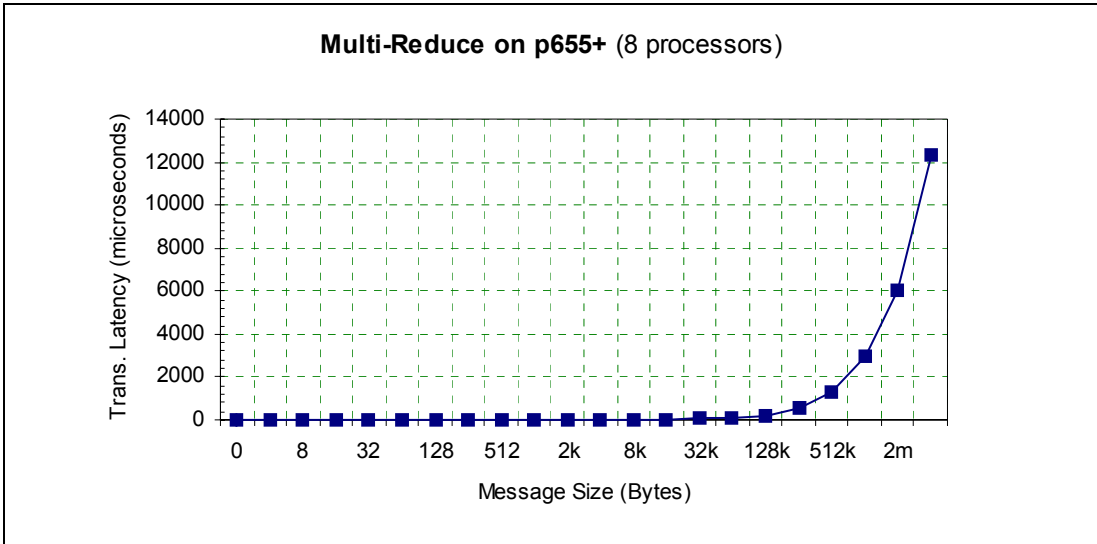
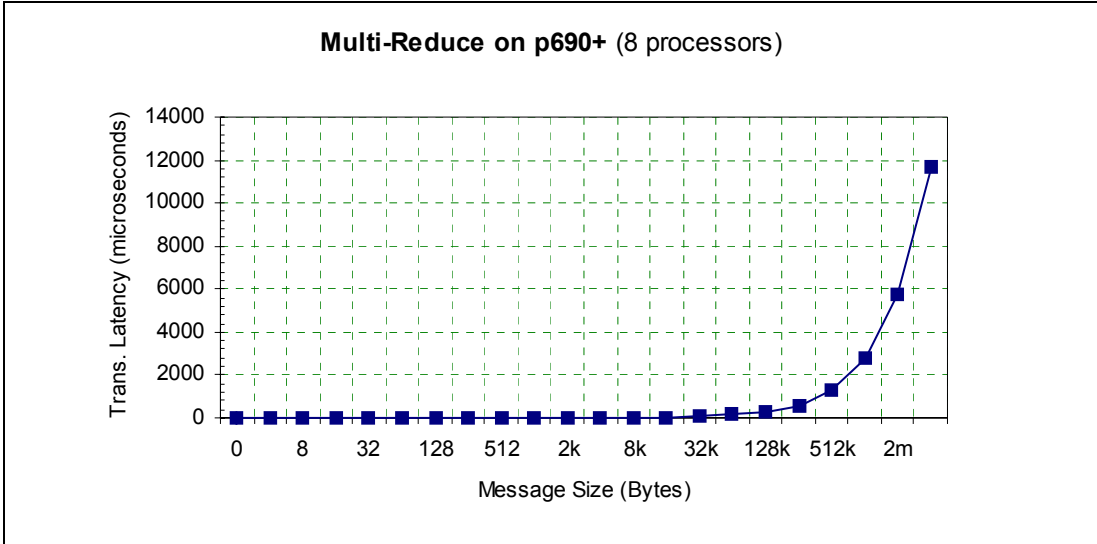
Collective or system-wide interconnect performance is measured between all or a subset of the nodes in the system. All collective benchmarks are measured in Microseconds transfer latency.

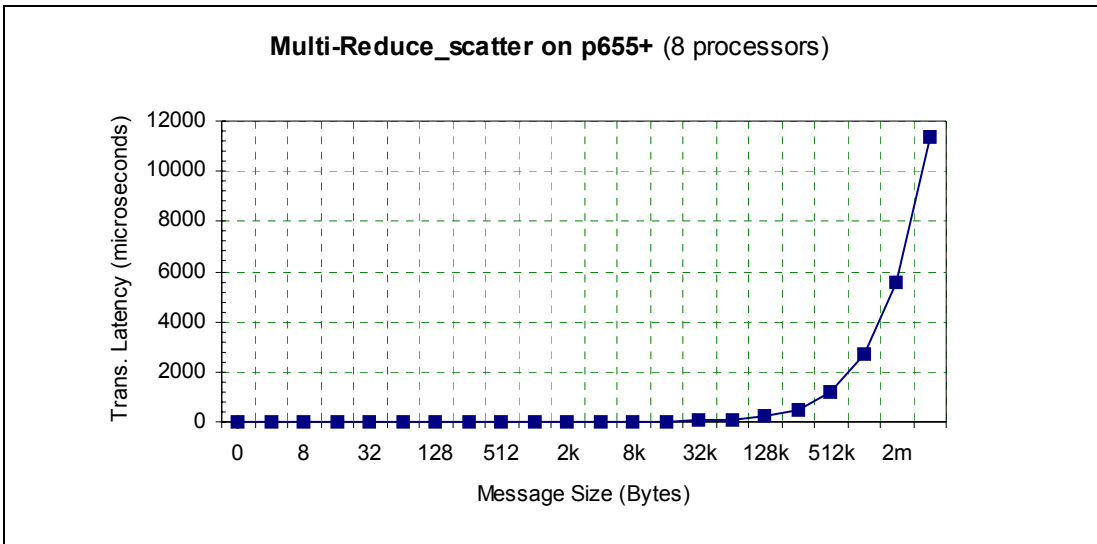
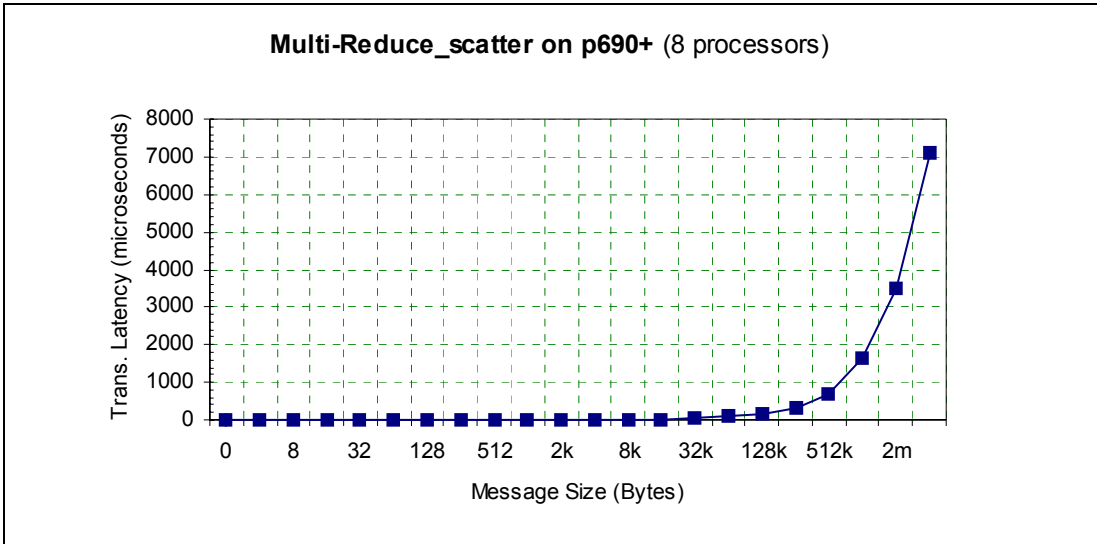
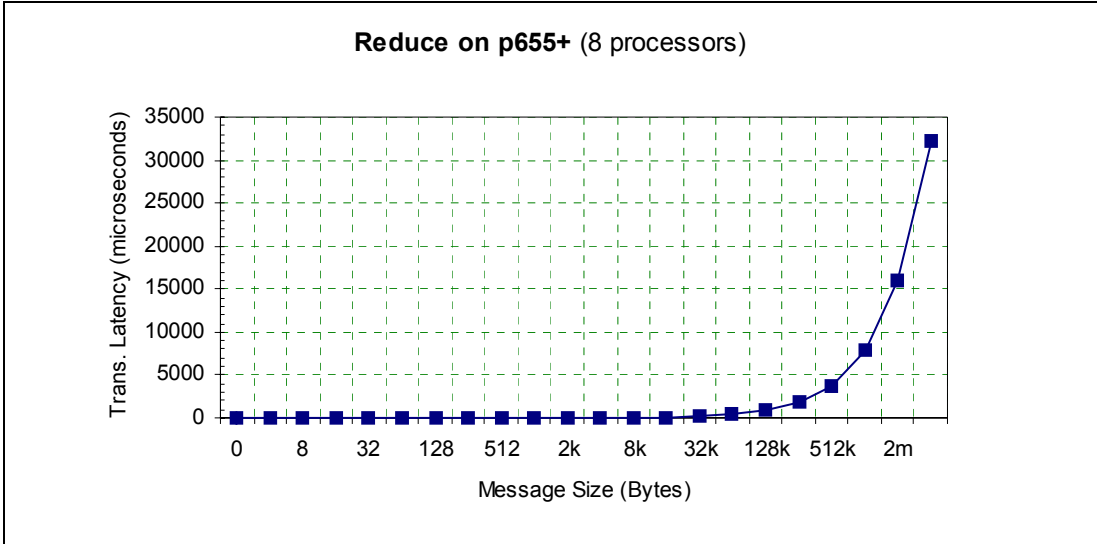
The following series of graphs illustrate the performance of Multi-All reduce, Allreduce, Multi-Reduce, Reduce, Multi-Reduce\_scatter, Reduce\_scatter, Multi-Allgather, Allgather, Multi-Allgatherv, Allgatherv, Multi-Alltoall, Alltoall, Multi-Bcast, and Bcast on p690+ and on p655+.

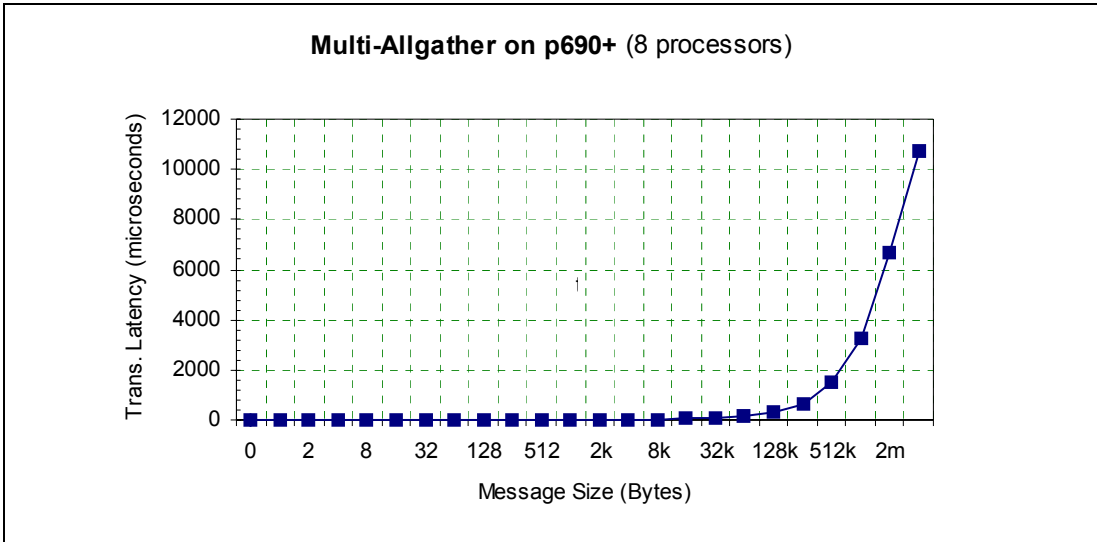
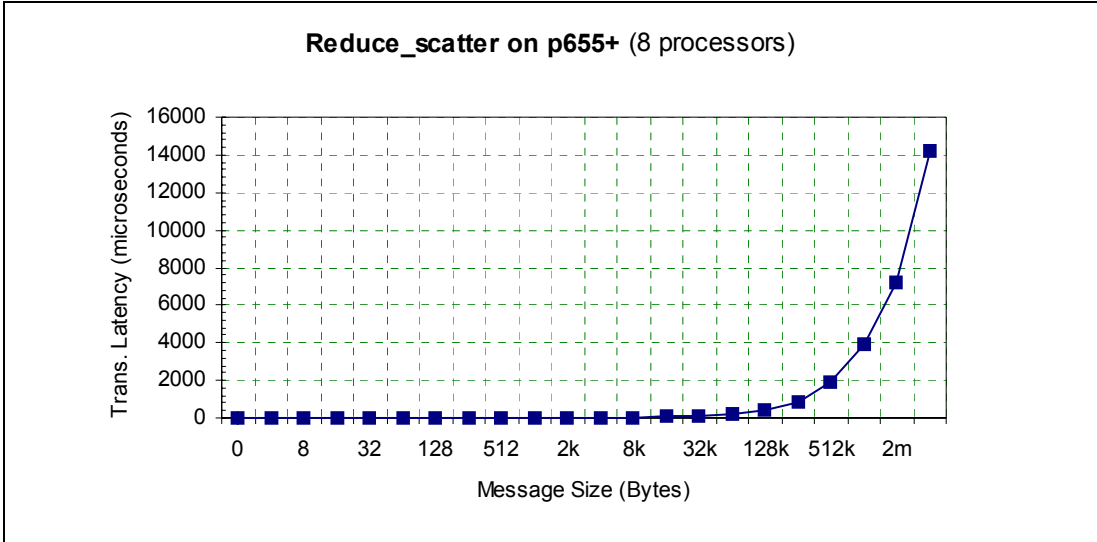
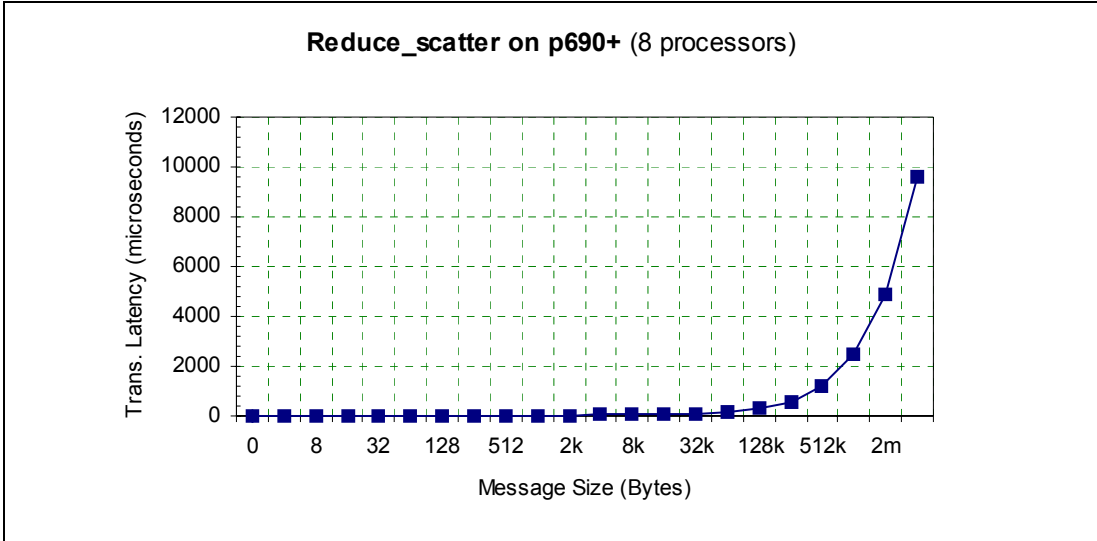


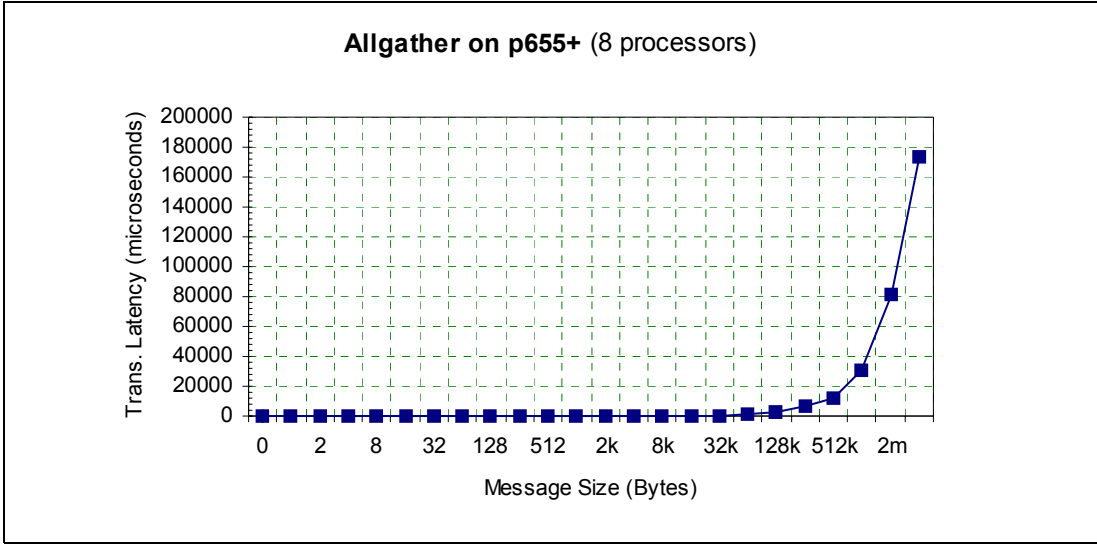
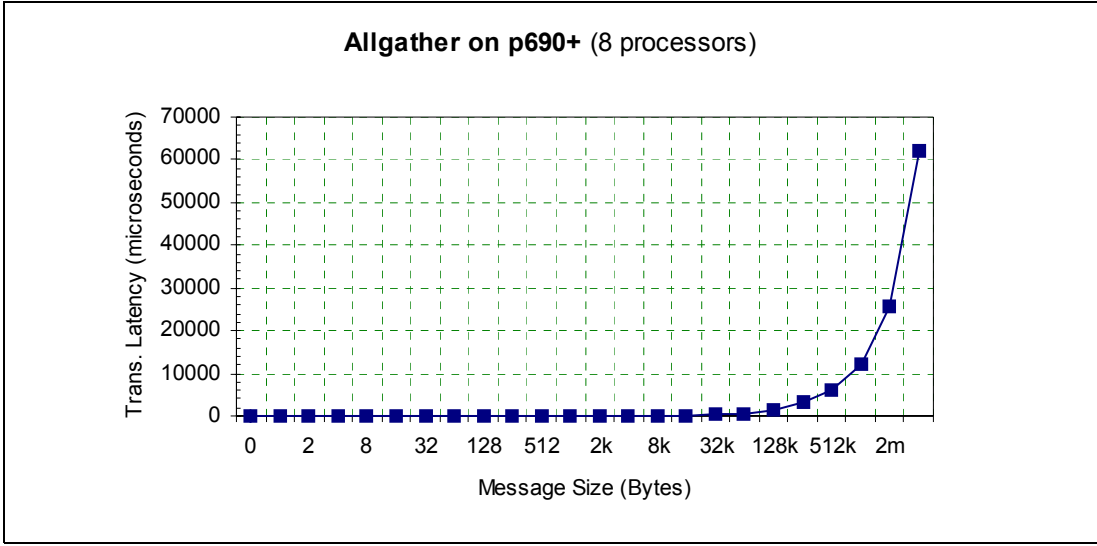
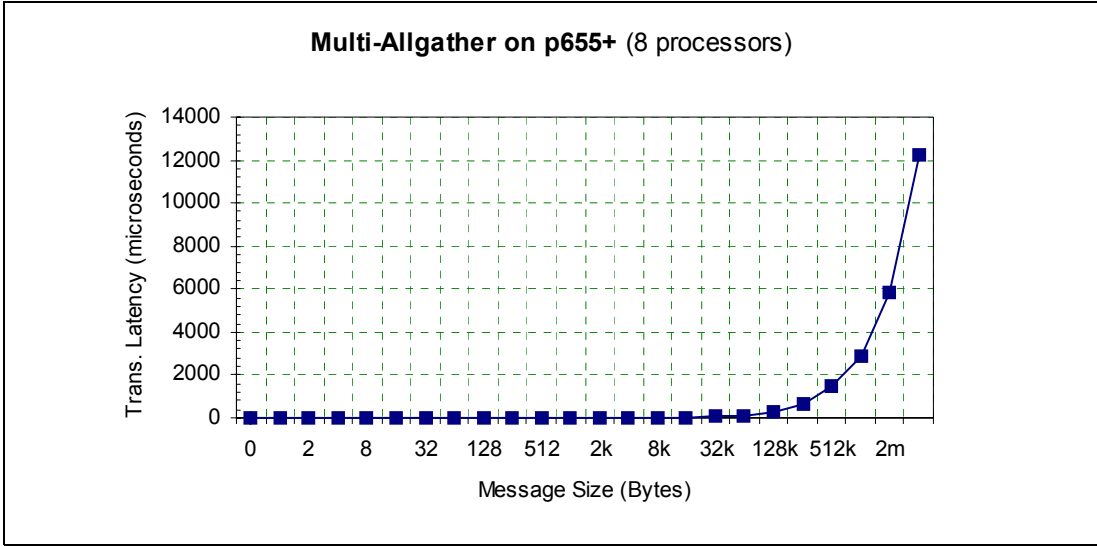


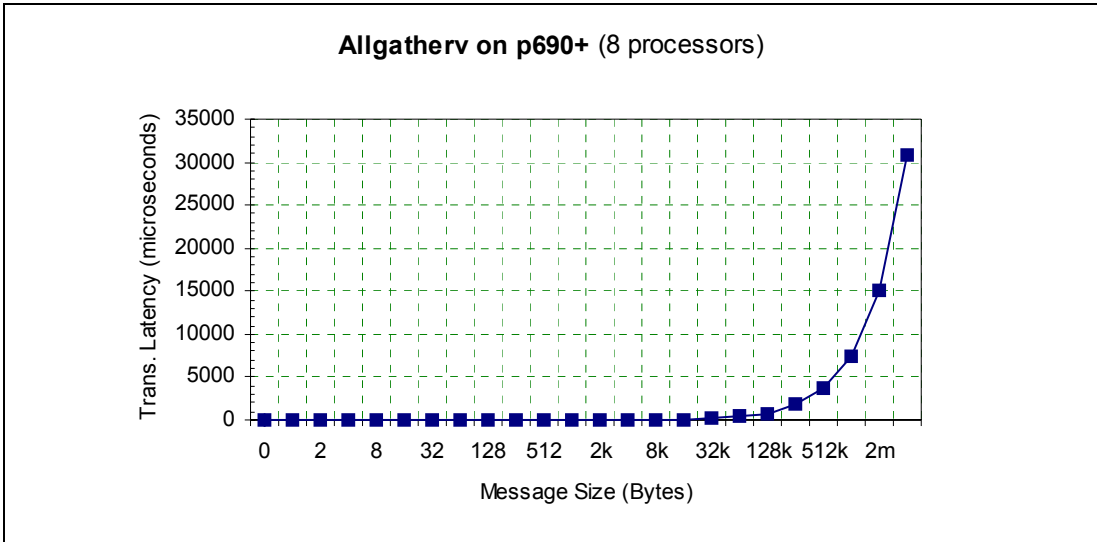
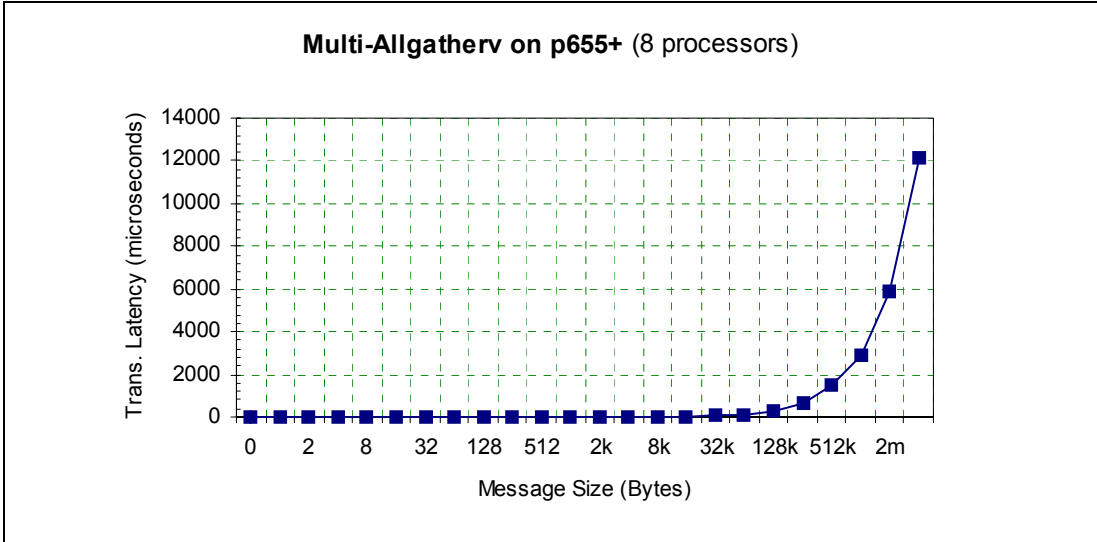
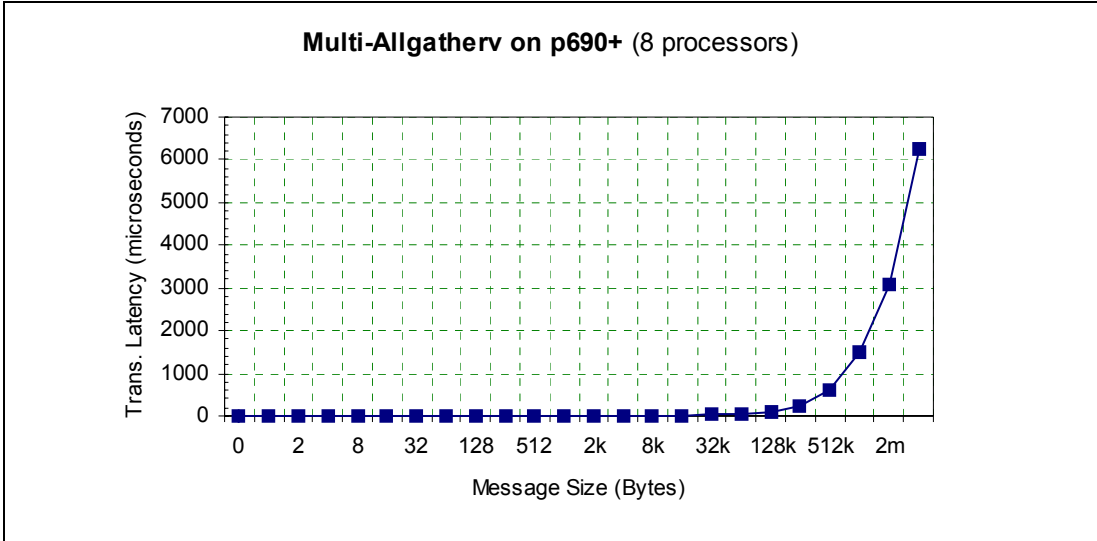


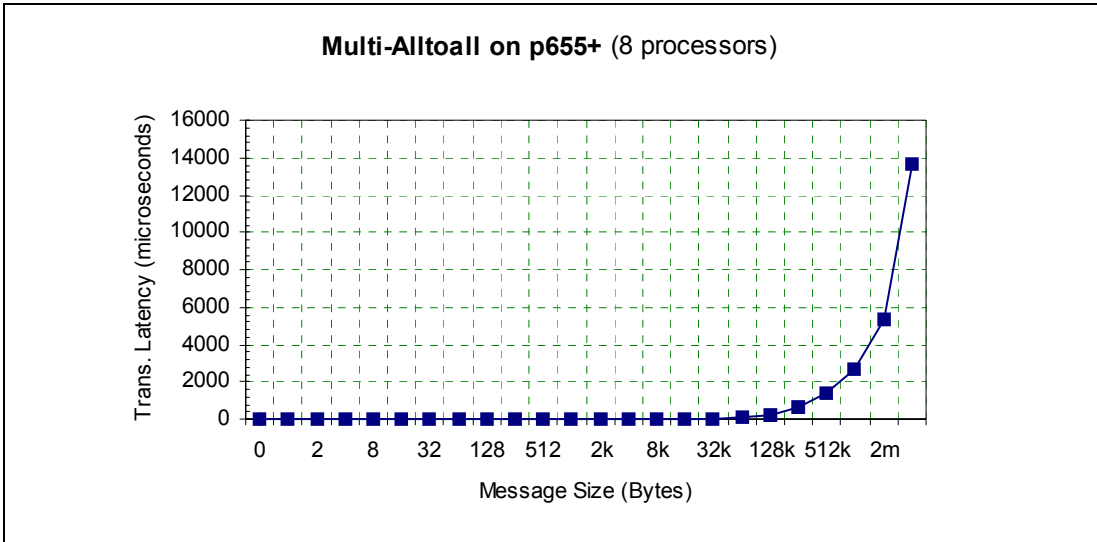
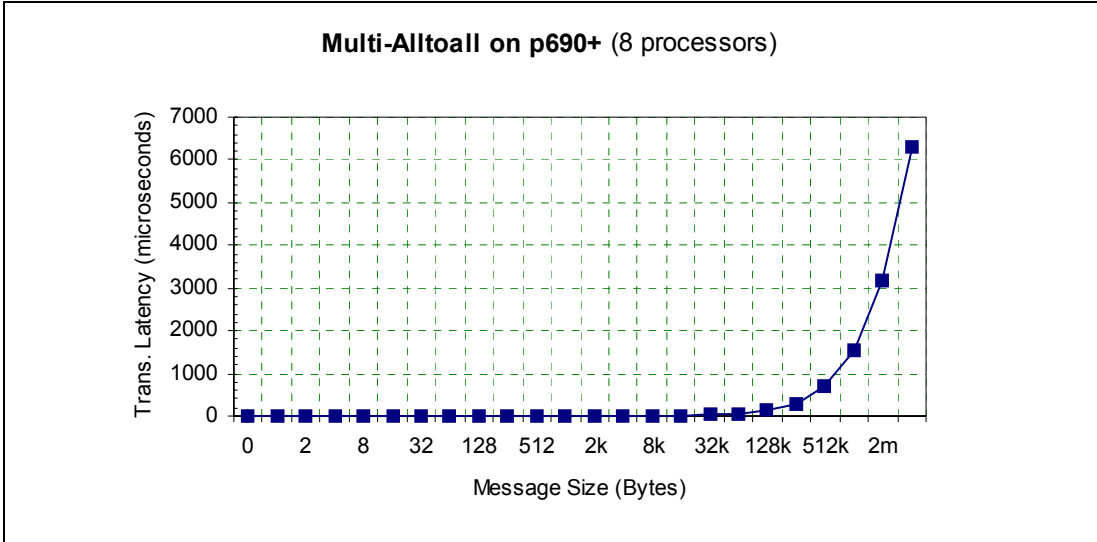
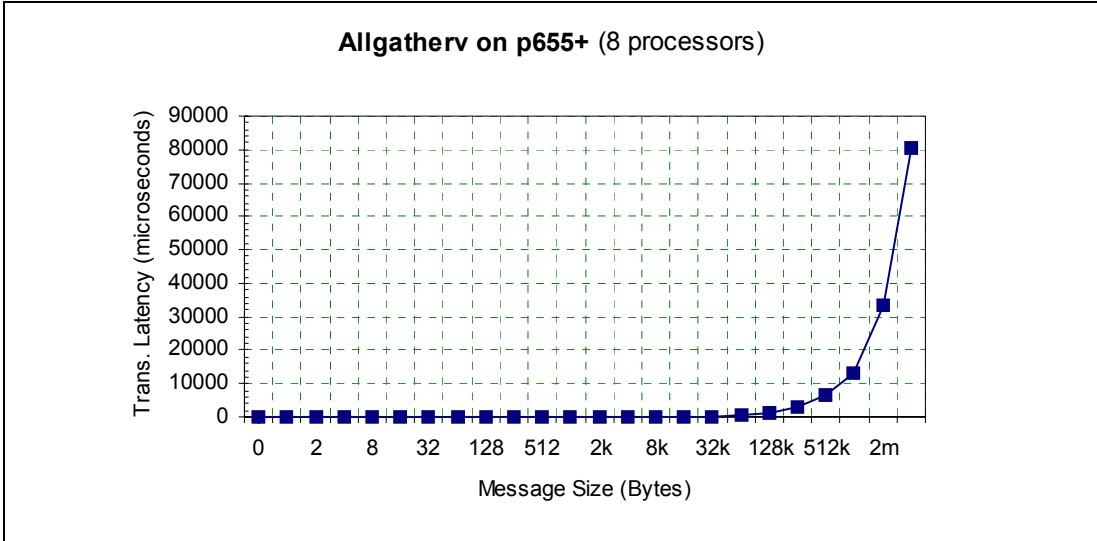


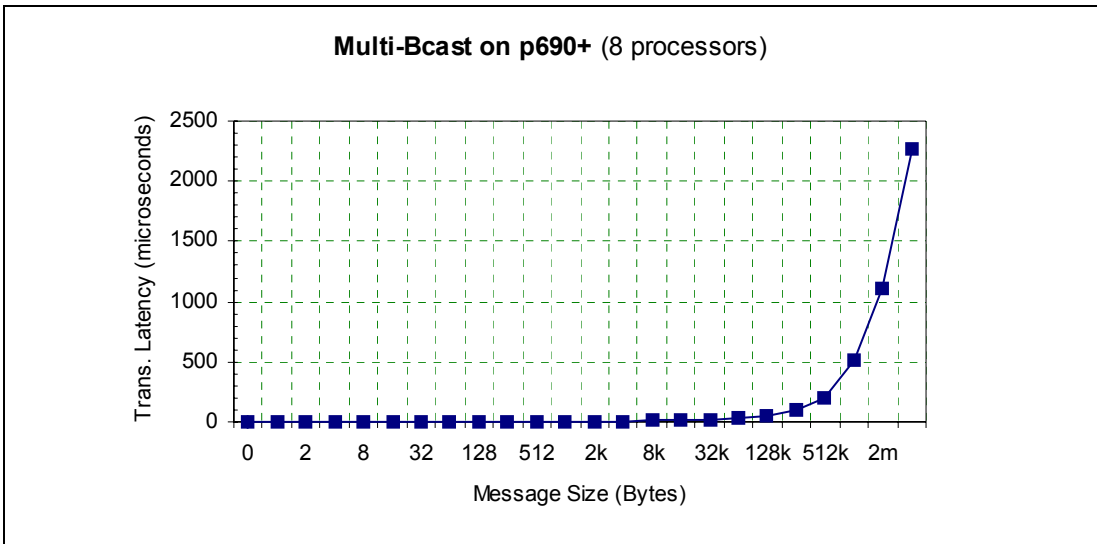
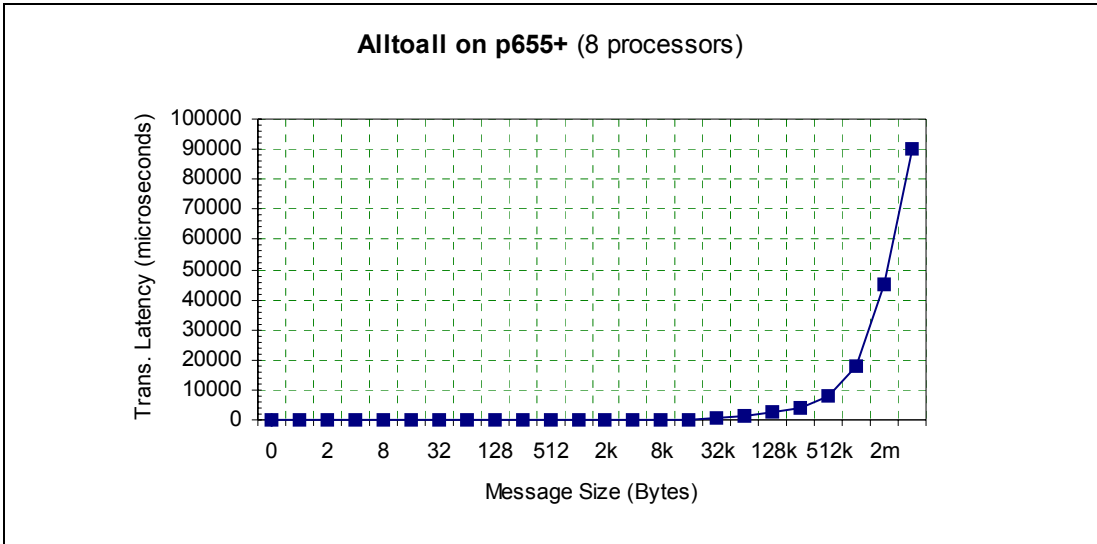
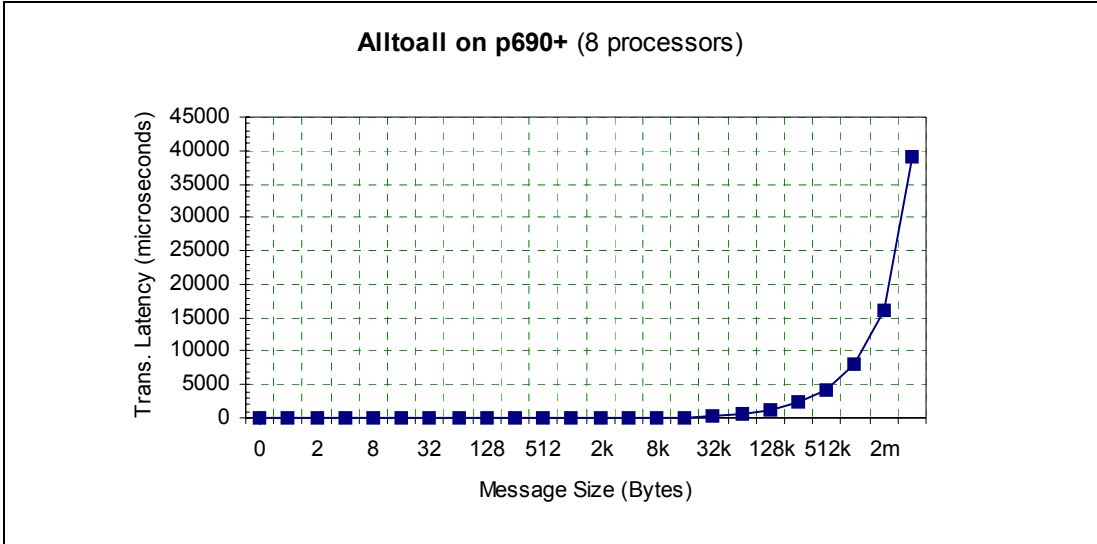


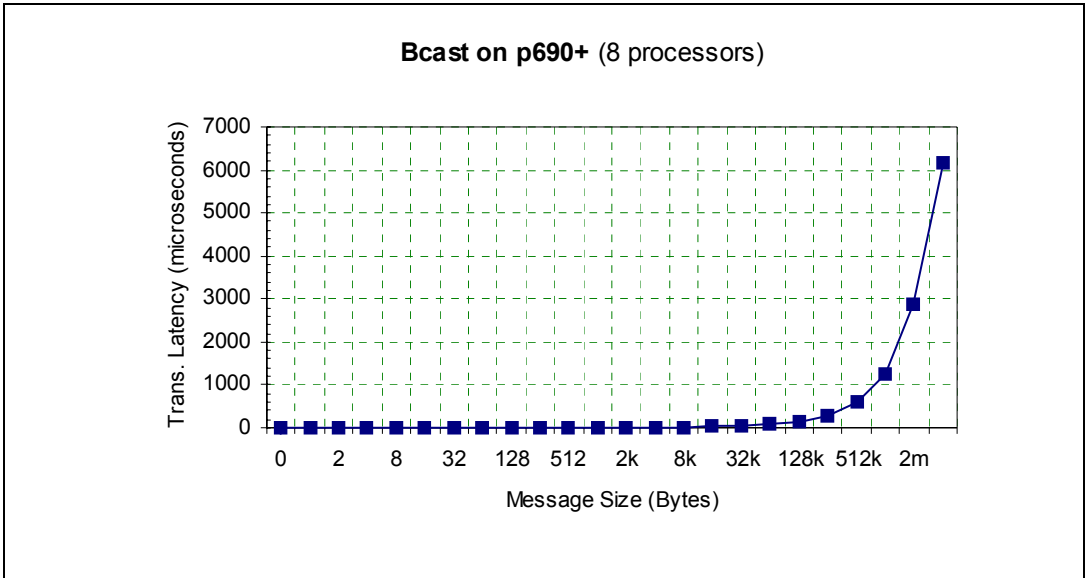
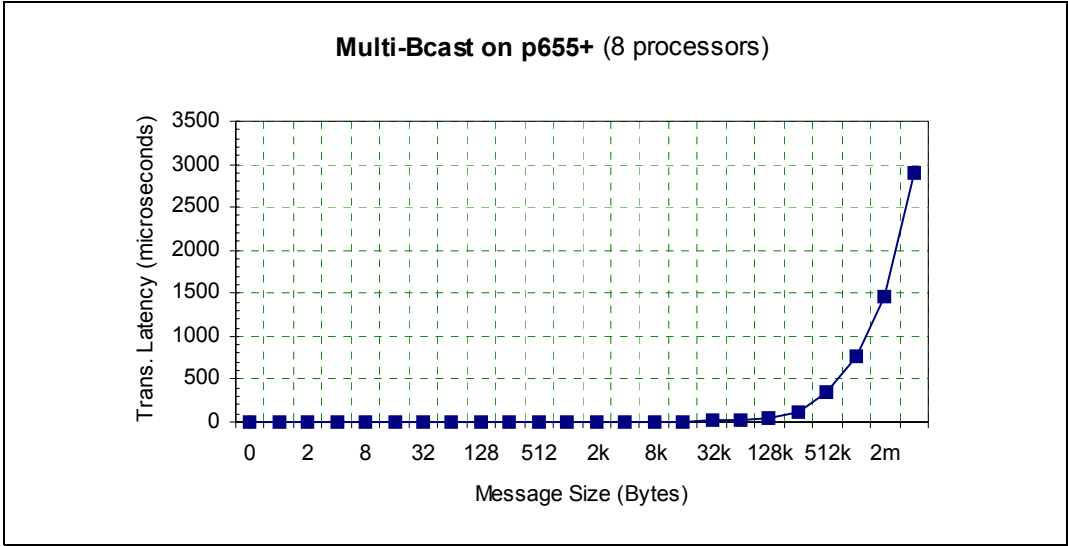




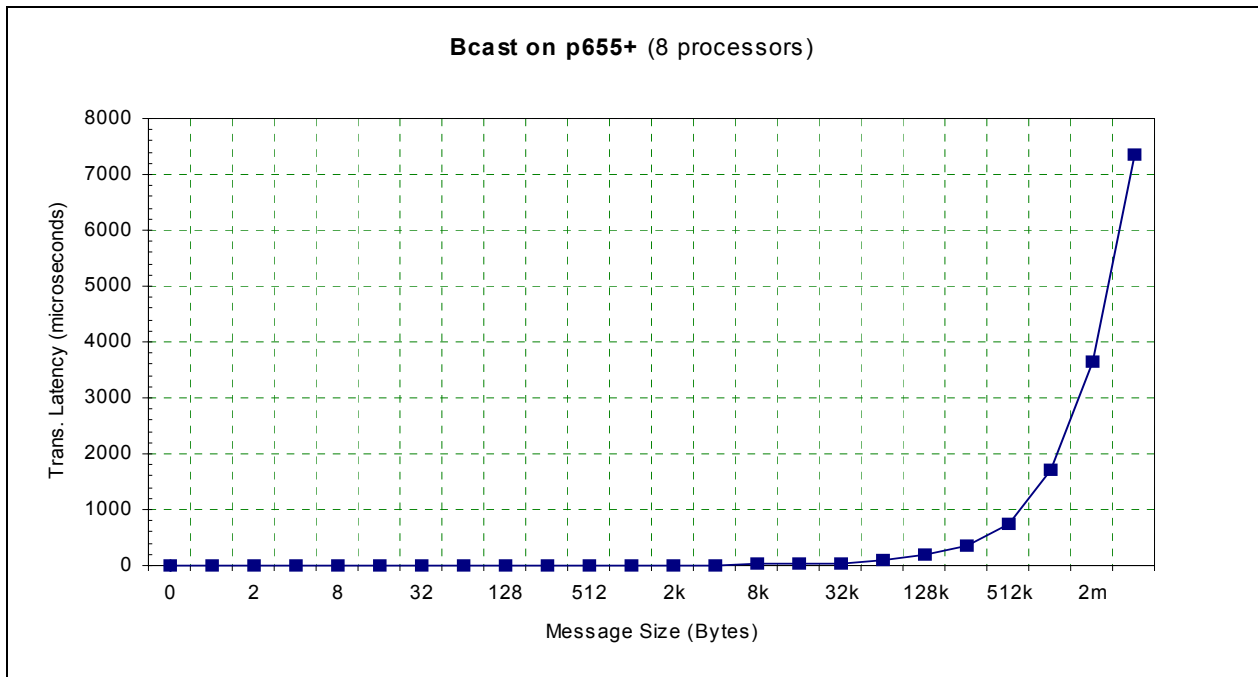












## MPI\_Barrier

Table 2 MPI\_Barrier () function benchmark

Test	Barrier (microseconds)				
	4	8	16	32	64
P690+	3.64	8.48	14.97	22.35	29.96
P655+	3.27	6.23	10.69	78.57	125.43

## Summary

The PMB-2.2.1 benchmark was completed on POWER4+ platforms p690+ and p655+. The MPI-I output results, without modification, are shown in graphical format in this report.

## Author

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## Reference

- ▶ PMB2.2.1-mpi:  
<http://www.pallas.com/e/products/pmb/index.htm>



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